

IMACS CPU / Interface Card Compatibility Matrix

				Interface Card										
	CPU Model	CPU Host Code Ver.	Ethernet Port	892060	892160	892260	892360	892460	892560	892660	892760	893270	893370	893470
Max T1/E1				8		8			2		2*	8		
EEPROM size (KB)				32		128			32			n/a**		
Modem				Y			Y			Y			Y	
Ext. Clock						Y						Y		
Node Port				Y	Y	Y	Y	Y		Y	Y	Y	Y	Y
Serial Port				Y	Y	Y	Y	Y		Y	Y	Y	Y	Y
Hot-swap Module												Y	Y	Y
3.x CPU	880060 [†]	3.x		✓	✓				✓	✓	✓			
	880160	3.x		✓	✓				✓	✓	✓			
	880460 [‡]	3.x		✓	✓				✓	✓	✓			
5.x CPU	880360	5.x				✓	✓	✓						
	880361	5.x	Y			✓	✓	✓						
6.x CPU	880370	6.x	Y			✓	✓	✓						
CPU7.x	880770	7.x	Y									✓	✓	✓

✓ = compatible / empty = not compatible

†: 880060 Supports 2 active T1/E1 w/o cross-connect. Non-redundant

‡: 880460 Supports 4 active T1/E1 and 4 redundant T1/E1 w/o cross-connect. First 2 T1/E1 w/ D&I & terminate support, next 2 T1/E1 terminate only.

*: E1 only

** : NVRAM located on CPU card in CPU-7; Only 128K available, remaining reserved for AMT